

GERMANIUM ALLOYED POWER TRANSISTOR

N-P-N power transistor in a metal envelope with the collector connected to the mounting base.

The AD161 is primarily intended for use together with the p-n-p power transistor AD162 as matched pair AD161/AD162 in 11 W complementary symmetry class B output stages of mains operated amplifiers and radio receivers.

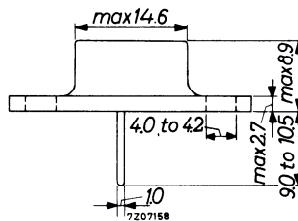
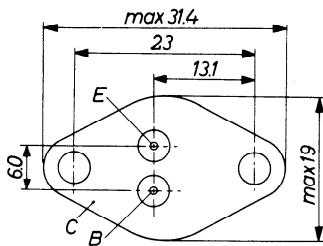
QUICK REFERENCE DATA

Collector-base voltage (open emitter)	V _{CBO}	max.	32	V
Collector-emitter voltage (open base)	V _{CEO}	max.	20	V
Collector current (peak value)	I _{CM}	max.	3	A
Total power dissipation up to T _{mb} = 75 °C	P _{tot}	max.	4	W
Junction temperature (incidentally)	T _j	max.	100	°C
D.C. current gain at T _j = 25 °C				
I _C = 0.5 A; V _{CE} = 1 V	h _{FE}		80 to 320	
Cut-off frequency				
I _C = 0.3 A; V _{CE} = 2 V	f _{hfe}	typ.	35	kHz

MECHANICAL DATA

Dimensions in mm

Collector connected to mounting base



Accessories and mounting instructions see page 4.

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RATINGS (Limiting values) ¹⁾Voltages

Collector-base voltage (open emitter)	V_{CBO}	max.	32	V
Collector-emitter voltage (open base)	V_{CEO}	max.	20	V
Collector-emitter voltage with $-V_{BE} = 0.6$ V (See also page A)	V_{CEX}	max.	32	V
Emitter-base voltage (open collector)	V_{EBO}	max.	10	V

Currents

Collector current (d.c. or average over any 50 ms period)	I_C	max.	1	A
→ Collector current (peak value)	I_{CM}	max.	3	A

Power dissipation

→ Total power dissipation up to $T_{mb} = 72$ °C	P_{tot}	max.	4	W
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Temperatures

Storage temperature	T_{stg}	-65 to +90	°C	
Junction temperature: continuous	T_j	max.	90	°C
incidentally	T_j	max.	100	°C

THERMAL RESISTANCE

→ From junction to mounting base	$R_{th\ j-mb}$	=	4.5	°C/W
From mounting base to heatsink with mica washer	$R_{th\ mb-h}$	=	1.5	°C/W
without mica washer	$R_{th\ mb-h}$	=	0.5	°C/W

¹⁾ Limiting values according to the Absolute Maximum System as defined in IEC publication 134. 7Z3 0943

CHARACTERISTICS $T_j = 25^\circ\text{C}$ unless otherwise specifiedCollector cut-off current

$I_E = 0; V_{CB} = 32 \text{ V}$	I_{CBO}	typ.	20	μA
$I_E = 0; V_{CB} = 32 \text{ V}; T_j = 90^\circ\text{C}$	I_{CBO}	<	500	μA
$-V_{BE} = 0.6 \text{ V}; V_{CE} = 32 \text{ V}; T_j = 90^\circ\text{C}$	I_{CEX}	<	3	mA

Emitter cut-off current

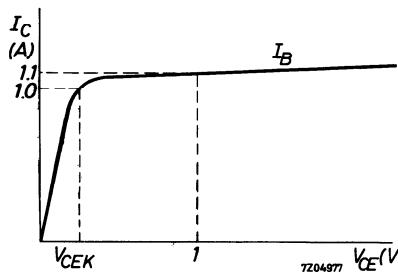
$I_C = 0; V_{EB} = 10 \text{ V}$	I_{EBO}	typ.	20	μA
$I_C = 0; V_{EB} = 10 \text{ V}; T_j = 90^\circ\text{C}$	I_{EBO}	<	200	μA

Base-emitter voltage 1)

$I_C = 5 \text{ mA}; V_{CE} = 10 \text{ V}$	V_{BE}	110 to 140	mV
$I_C = 50 \text{ mA}; V_{CE} = 1 \text{ V}$	V_{BE}	<	300 mV
$I_C = 500 \text{ mA}; V_{CE} = 1 \text{ V}$	V_{BE}	<	650 mV
$I_C = 2 \text{ A}; V_{CE} = 1 \text{ V}$	V_{BE}	<	1100 mV ←

Knee voltage

$I_C = 1 \text{ A}; I_B = \text{value for which}$	V_{CEK}	<	600	mV
$I_C = 1.1 \text{ A at } V_{CE} = 1 \text{ V}$				←

Floating voltage

$I_E = 0; V_{CB} = 32 \text{ V}; T_j = 90^\circ\text{C}$	V_{EBfl}	<	400	mV
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Collector capacitance at $f = 450 \text{ kHz}$

$I_E = I_e = 0; V_{CB} = 5 \text{ V}$	C_C	typ.	150	pF
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1) V_{BE} decreases by about 2 mV/ $^\circ\text{C}$ with increasing temperature.

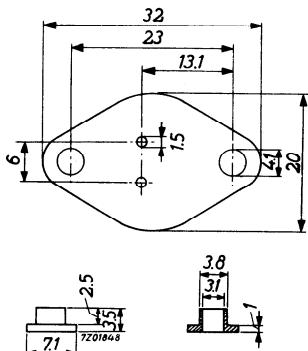
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CHARACTERISTICS (continued) $T_j = 25^\circ\text{C}$ unless otherwise specified**→ D.C. current gain** $I_C = 5 \text{ mA}; V_{CE} = 10 \text{ V}$ h_{FE} > 55 $I_C = 50 \text{ mA}; V_{CE} = 1 \text{ V}$ h_{FE} 74 to 300 $I_C = 500 \text{ mA}; V_{CE} = 1 \text{ V}$ h_{FE} typ. 150
80 to 320 $I_C = 2 \text{ A}; V_{CE} = 1 \text{ V}$ h_{FE} > 40**Transition frequency** $I_C = 10 \text{ mA}; V_{CE} = 2 \text{ V}$ f_T typ. 3 MHz**Cut-off frequency** $I_C = 300 \text{ mA}; V_{CE} = 2 \text{ V}$ f_{hfe} > 20 kHz
typ. 35 kHz**D.C. current gain ratio**

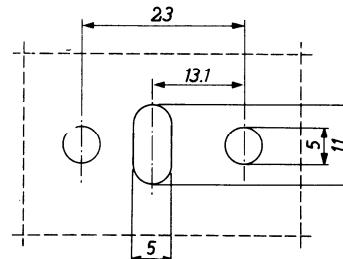
of matched pair AD161/AD162

 $I_C = 500 \text{ mA}; V_{CE} = 1 \text{ V}$ h_{FE1}/h_{FE2} typ. 1.1
< 1.25**ACCESSORIES AND MOUNTING INSTRUCTIONS**

Dimensions in mm



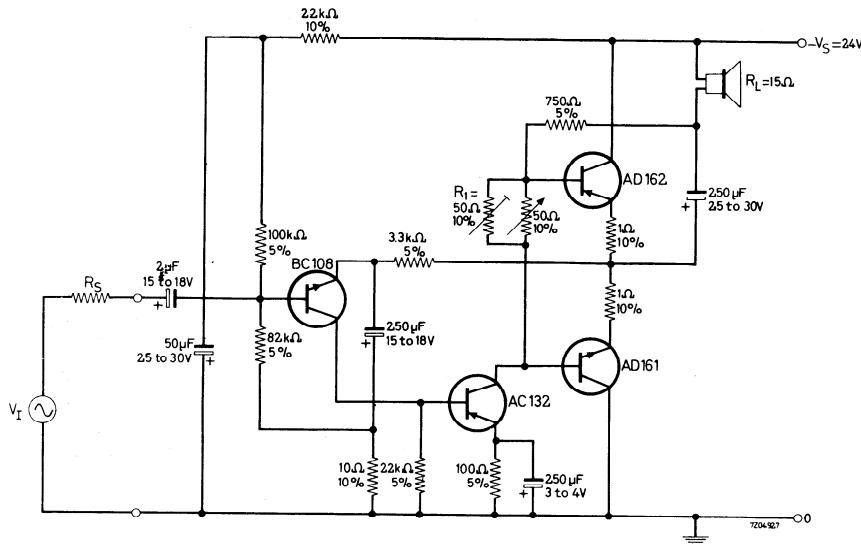
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Mica washer (50 to 100 μm)
and insulation bushesBore-hole dimensions
for heatsink

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APPLICATION INFORMATION

4 W transformerless audio frequency amplifier with matched pair AD161/AD162 in complementary symmetry class B output stage.



Typical input requirements
for an output power of 4 W

$V_i(\text{rms}) = 28 \text{ mV}$; $I_i(\text{rms}) = 0.7 \mu\text{A}$;
 $R_i = 40 \text{ k}\Omega$; $T_{\text{amb}} \text{ max. } 45^\circ\text{C}$

Typical bandwidth (3 dB)

$B = 70 \text{ Hz to } 16 \text{ kHz}$

Quiescent current

$I_{CQ} = 8 \text{ mA}$, adjustable with R_1

Heatsink for AC132

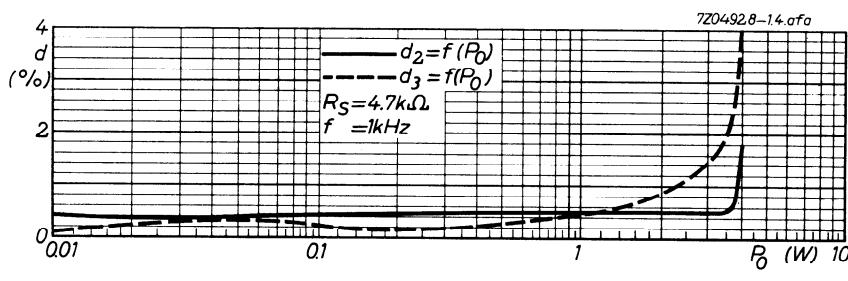
$10 \text{ cm}^2 \text{ Al, thickness } 1 \text{ mm}$

Heatsinks for AD161 and AD162

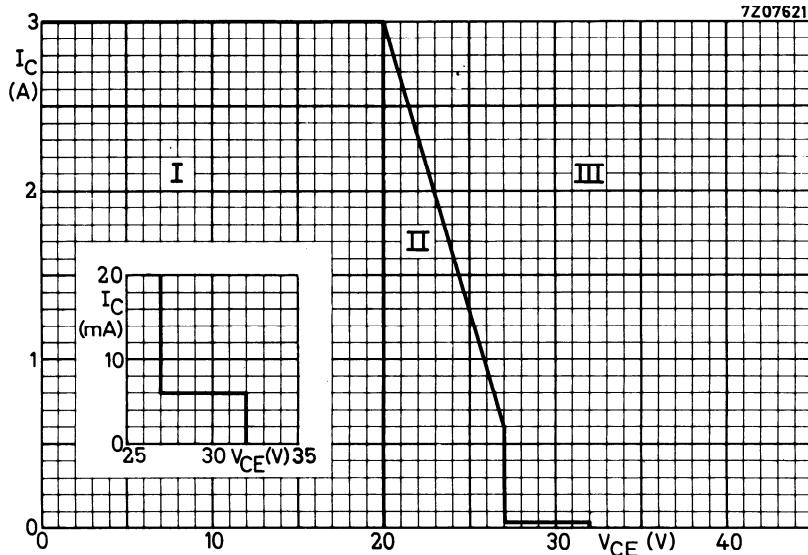
$R_{\text{th h-a}} < 14.5^\circ\text{C/W}$

Supply voltage

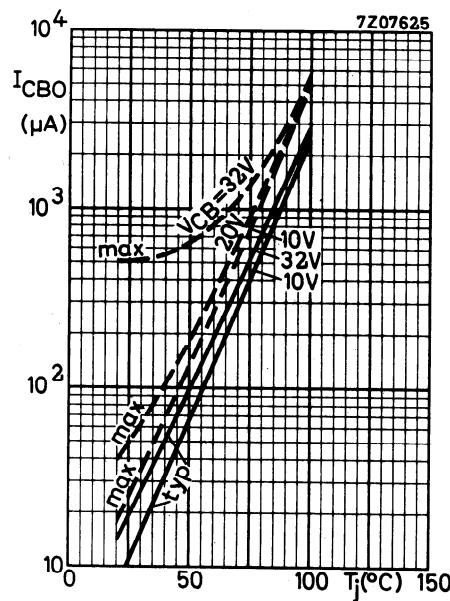
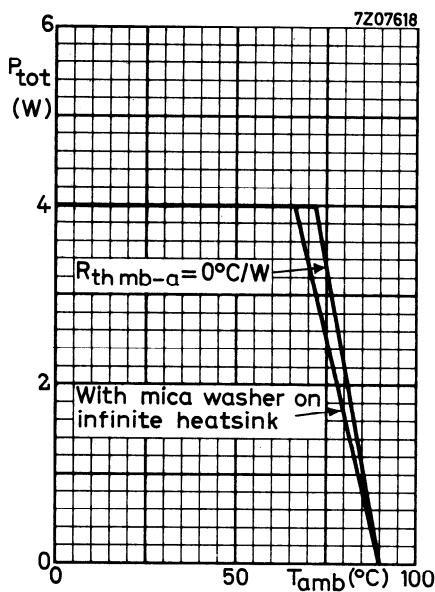
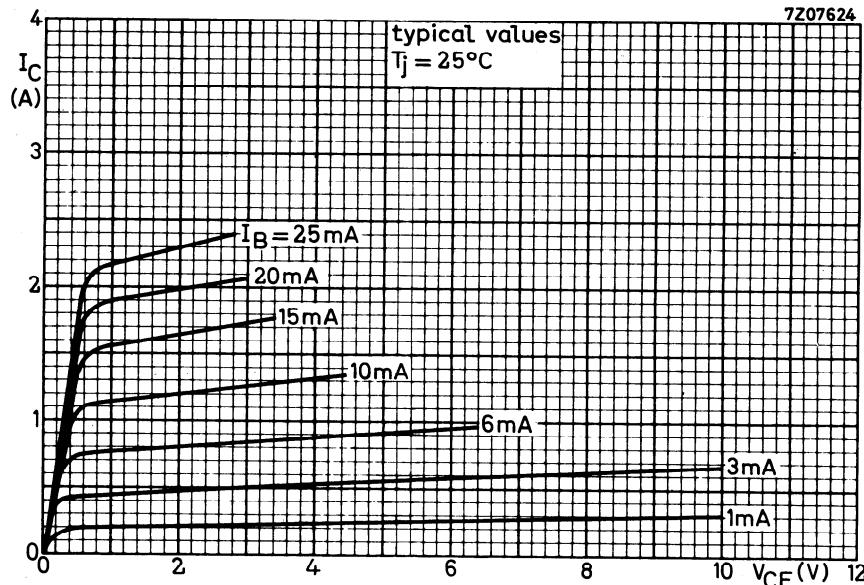
$-V_S = 24 \text{ V, max. } 27 \text{ V}$



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- I = Region of permissible operation under all base-emitter conditions.
II = Additional region of operation when the transistor is cut-off with
 $-V_{BE} \geq -V_{BEfl}$.
III = Outside regions I and II, the transistor can withstand transient energies
of 1 mWs, provided it is cut-off with $-V_{BB} \leq 0.6$ V; $R_i = 18 \Omega$.



AD161

AD161/AD162

