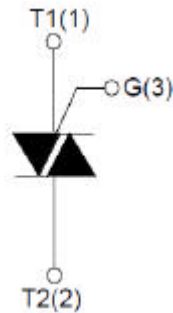
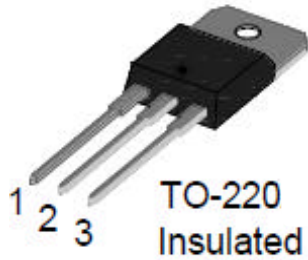


8A TRIACs



BTA08 - 600/800/1200

TO-220
Insulated Plastic Package

BTA08 Series Triacs, with high ability to withstand the shock loading of large current, provide high dV/dt rate with strong resistance to electromagnetic interference. With high commutation performances, 3 Quadrants products especially recommended for use on Inductive Load. It provides Insulation voltage rated at 2500V RMS from all three terminals to external heatsink complying with UL standards.

ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	VALUE	UNIT
Repetitive Peak Off-State Voltage ($T_j=25^\circ\text{C}$)	V_{DRM}	600 / 800 / 1200	V
Repetitive Peak Reverse Voltage ($T_j=25^\circ\text{C}$)	V_{RRM}	600 / 800 / 1200	V
Non Repetitive Surge Peak Off-State Voltage	V_{DSM}	$V_{\text{DRM}} + 100$	V
Non Repetitive Peak Reverse Voltage	V_{RSM}	$V_{\text{RRM}} + 100$	V
RMS On-State Current ($T_c = 100^\circ\text{C}$)	$I_{\text{T(RMS)}}$	8	A
Non Repetitive Surge Peak On-State Current (Full Cycle, $f = 50\text{Hz}$)	I_{TSM}	80	A
I^2t Value For Fusing ($t_p=10\text{ms}$)	I^2t	32	A^2s
Critical Rate of Rise of On-State Current ($I_G = 2 \times I_{\text{GT}}$)	di/dt	50	$\text{A}/\mu\text{s}$
Peak Gate Current	I_{GM}	4	A
Average Gate Power Dissipation	$P_{\text{G(AV)}}$	1	W
Peak Gate Power	P_{GM}	5	W
Storage Junction Temperature Range	T_{STG}	-40 to +150	$^\circ\text{C}$
Operating Junction Temperature Range	T_j	-40 to +125	$^\circ\text{C}$

THERMAL RESISTANCE

Maximum Thermal Resistance Junction to case	$R_{\text{th(j-c)}}$	4.0	$^\circ\text{C}/\text{W}$
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ELECTRICAL CHARACTERISTICS (T_j = 25°C unless otherwise specified)

3 Quadrants

PARAMETER	TEST CONDITION	SYMBOL	QUADRANT	VALUES				UNIT
				BTA08				
				TW	SW	CW	BW	
Gate Trigger Current	V _D =12V, R _L =33Ω	I _{GT}	I - II - III	< 5	< 10	< 35	< 50	mA
Gate Trigger Voltage		V _{GT}	I - II - III	< 1.5				V
Off-State Gate Voltage	V _D =V _{DRM} , T _j =125°C, R _L = 3.3KΩ	V _{GD}	I - II - III	> 0.2				V
Latching Current	I _G =1.2 X I _{GT}	I _L	I - III	< 15	< 20	< 50	< 70	mA
			II	< 25	< 35	< 60	< 80	
Holding Current	I _{TM} = 100mA	I _H		< 10	< 15	< 40	< 60	mA
Critical Rate of Rise of Off-State Voltage	V _D = 2/3 V _{DRM} , Gate Open, T _j =125°C	dV/dt		> 50	> 200	> 500	> 1000	V/μs

4 Quadrants

PARAMETER	TEST CONDITION	SYMBOL	QUADRANT	VALUES		UNIT
				BTA08		
				C	B	
Gate Trigger Current	V _D =12V, R _L =33Ω	I _{GT}	I - II - III	< 25	< 50	mA
			IV	< 50	< 70	
Gate Trigger Voltage		V _{GT}	ALL	< 1.5		V
Off-State Gate Voltage	V _D =V _{DRM} , T _j =125°C, R _L = 3.3KΩ	V _{GD}	ALL	> 0.2		V
Latching Current	I _G =1.2 X I _{GT}	I _L	I - III - IV	< 35	< 50	mA
			II	< 60	< 80	
Holding Current	I _T = 200mA	I _H		< 25	< 50	mA
Critical Rate of Rise of Off-State Voltage	V _D = 2/3 V _{DRM} , Gate Open, T _j =125°C	dV/dt		> 200	> 500	V/μs

STATIC CHARACTERISTICS

PARAMETER	TEST CONDITION	SYMBOL		VALUE	UNIT
				BTA08	
On-State Voltage	I _{TM} =11A, t _p =380μs	V _{TM}	T _J =25°C	< 1.55	V
Off-State Leakage Current	V _D = V _{DRM} , V _R = V _{RDM}	I _{DRM} / I _{RDM}	T _J = 25°C	< 5	μA
			T _J = 125°C	< 1	mA

CHARACTERISTICS CURVES

FIG.1 Maximum power dissipation versus RMS on-state current

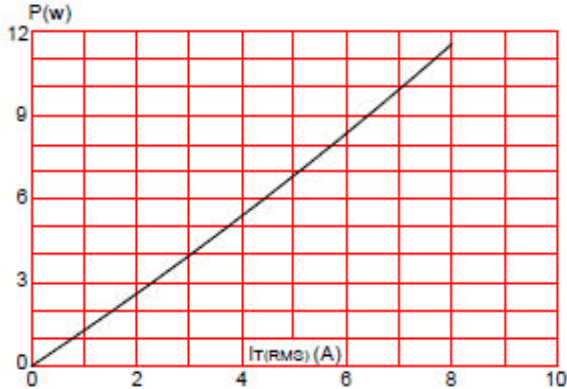


FIG.2: RMS on-state current versus case temperature

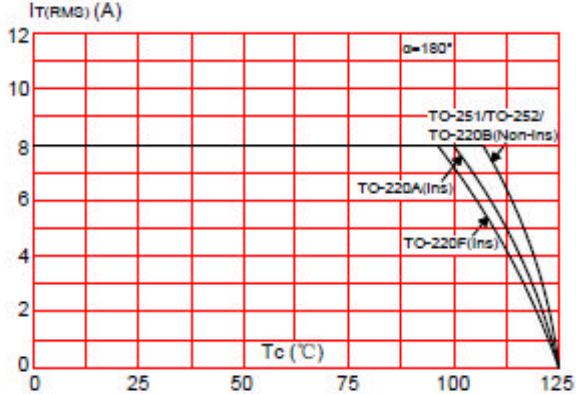


FIG.3: Surge peak on-state current versus number of cycles

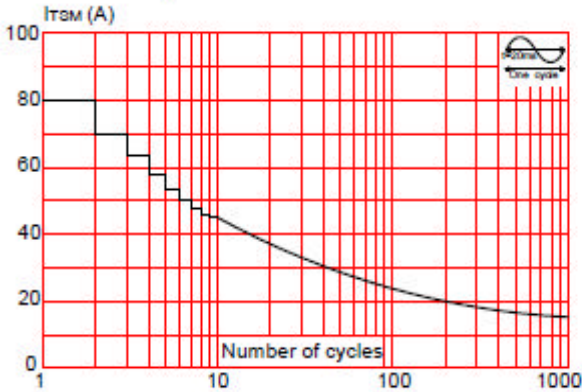


FIG.4: On-state characteristics (maximum values)

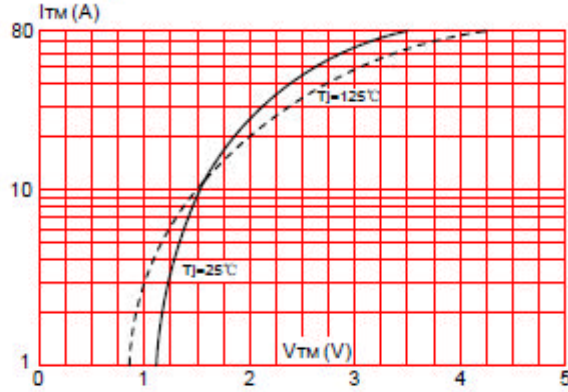


FIG.5: Non-repetitive surge peak on-state current for a sinusoidal pulse with width $t_p < 20\text{ms}$, and corresponding value of I^2t ($dI/dt < 50\text{A}/\mu\text{s}$)

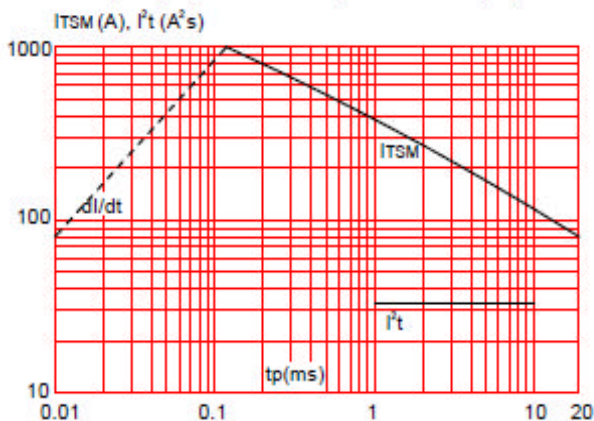
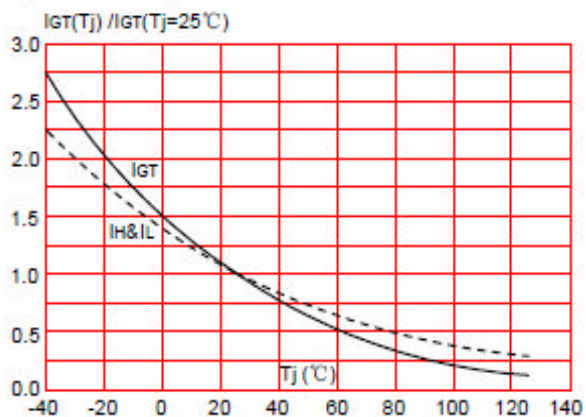
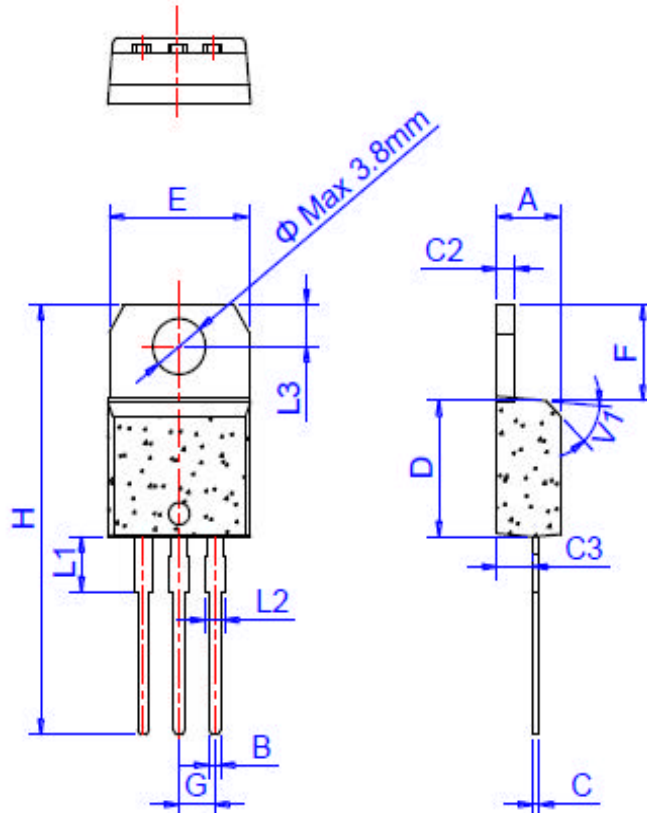


FIG.6: Relative variations of gate trigger current, holding current and latching current versus junction temperature



TO-220 (INSULATED) PACKAGE OUTLINE AND DIMENSION



Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	4.40		4.60	0.173		0.181
B	0.61		0.88	0.024		0.035
C	0.46		0.70	0.018		0.028
C2	1.21		1.32	0.048		0.052
C3	2.40		2.72	0.094		0.107
D	8.60		9.70	0.339		0.382
E	9.80		10.4	0.386		0.409
F	6.55		6.95	0.258		0.274
G		2.54			0.1	
H	28.0		29.8	1.102		1.173
L1		3.75			0.148	
L2	1.14		1.70	0.045		0.067
L3	2.65		2.95	0.104		0.116
V1		45°			45°	



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Customer Notes

Component Disposal Instructions

1. CDIL Semiconductor Devices are RoHS compliant, customers are requested to please dispose as per prevailing Environmental Legislation of their Country.
2. In Europe, please dispose as per EU Directive 2002/96/EC on Waste Electrical and Electronic Equipment (WEEE).

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The product information and the selection guides facilitate selection of the CDIL's Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD is believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

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